

DELTA AI

NVIDIA GH200

NCSA DeltaAI Project

NSF Award OAC- 2320345

Designed for today's Artificial Intelligence and
Machine Learning research needs



NAIRR Pilot

National Artificial Intelligence
Research Resource Pilot

Code of Conduct

- ACCESS Code of Conduct

As a program that aims to share ideas and freedom of thought and expression, it is essential that the interaction between participants, users of ACCESS services and ACCESS staff take place in an environment that recognizes the inherent worth of every person by being respectful of all. All ACCESS participants strive to be empathetic, respectful, welcoming, friendly, and patient. We strive to be collaborative and use language that reflects our values.

The ACCESS program does not tolerate harassment in any form. Harassment is any form of behavior intended to exclude, intimidate or cause discomfort. Harassment includes, but is not limited to, the use of abusive or degrading language, intimidation, stalking, harassing photography or recording, inappropriate physical contact, and unwelcome sexual attention.

<https://access-ci.org/code-of-conduct/>

- UIUC Code of Conduct

https://www.ethics.uillinois.edu/compliance/university_code_of_conduct

DELTA AI OFFERS:

- 456 H100 NVIDIA GPUs
- 200 Gb/s HPE SlingShot network fabric
- Two Lustre file systems (based on HDD and NVME, respectively) shared with Delta to support both block and small file IO.
- Access to project space on the “Taiga” Lustre based center wide project file system
- Home directories provisioned on the “Harbor” VAST based center wide home directory system.

114 GPU nodes consisting of:

- 4 Grace Hopper GH200 superchips per node
- Each GH200 superchip has one H100 GPU and a 72-core Grace ARM CPU.
- Each H100 has 96GB HBM3
- Each Grace ARM CPU has 120GB of LPDDR5X memory
- 4 Slingshot11 network connections: 1 per Grace Hopper superchip
- One 3.5 TB NVME drive per node

Dr. John Linford, NVIDIA

Experience



Principal Technical Product Manager, Datacenter CPU Software

NVIDIA · Full-time

Apr 2022 - Present · 2 yrs 5 mos

Austin, Texas, United States



Arm

4 yrs 3 mos

Austin, Texas Area

- **Director, HPC Engineering**

Full-time

Oct 2019 - Apr 2022 · 2 yrs 7 mos

Lead and manage a worldwide team of HPC engineers with the ultimate goal of making Arm a win for HPC and vice versa. Charged with informing Arm's IP roadmap and representing the needs of the HPC ...see more